

GAU2783

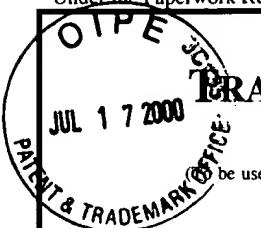
Please type a plus sign inside this box -->

PTO/SB/21 (12/97)

Approved for use through 09/30/00 OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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TRANSMITTAL FORM

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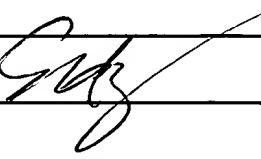
Application Number	098/887,680
Filing Date	7/30/97
First Named Inventor	Powell
Group Art Unit	2783
Examiner Name	Eng
Attorney Docket Number	82225.P0189R

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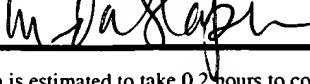
ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers	<input type="checkbox"/> After Allowance Communication Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawings	<input type="checkbox"/> Appeal Communication to Board of Appeals & Interferences
<input checked="" type="checkbox"/> Amendment/Response	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition Routing Slip (PTO/SB/69) & Accompanying Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Petition Checklist & Accompanying Petition	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> To Convert a Provisional Application	<input type="checkbox"/> Additional Enclosure(s) (please identify below)
<input type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> Power of Attorney, Revocation, Change of Correspondence Address	<input type="checkbox"/> Declaration and Power of Attorney
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Terminal Disclaimer	
<input type="checkbox"/> Response to Missing Parts/Incomplete Application	<input type="checkbox"/> Small Entity Request	
<input type="checkbox"/> Response to Missing Parts Under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Request for Refund	
<u>Remarks</u>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual Name	BLAKELY SOKOLOFF TAYLOR & ZAFMAN ERIC S. HYMAN
Signature	
Date	July 14, 2000

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date:		July 14, 2000
Typed or Printed Name		ERIC S. HYMAN, ESQ.
Signature		Date

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.



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PTO/SB/17 (12/97)

Approved for use through 09/30/00 OMB 0651-0032
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

FEE TRANSMITTAL

Note: Effective October 1, 1997

Patent fees are subject to annual revision

TOTAL AMOUNT OF PAYMENT

\$100

Complete If Known

08/887,680

7/3/97

Powell

2783

Eng

82225.P0189R

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METHOD OF PAYMENT (check one)

1. The Commissioner is hereby authorized to charge indicated fees & credit any overpayments to:

Acct # 02-2666

Acct Name Blakely Sokoloff Taylor & Zafman

Charge any add'l fee required under 37 CFR 1.16 & 1.17 Charge issue fee set in 37 CFR 1.18 at the mailing of the Notice of Allowance

FEE CALCULATION (continued)

Additional Fees

Large Entity

Small Entity

Code	Fee (\$)	Code	Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge-late filing fee or oath	
127	50	227	25	Surcharge-late provisional filing fee or cover sheet	
139	130	139	130	Non-English specification	
147	2520	147	2520	For filing a request for reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1840*	113	1840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within 1st month	
116	380	216	190	Extension for reply within 2nd month	
117	870	217	435	Extension for reply within 3rd month	
118	1360	218	680	Extension for reply within 4th month	
128	1850	228	925	Extension for reply within 5th month	
119	300	219	150	Notice of Appeal	
120	300	220	150	Filing a brief in support of appeal	
121	260	221	130	Request for oral hearing	
138	1510	138	1510	Petition to institute a public use proceeding	
140	110	240	55	Petition to Revive-unavoidable	
141	1210	241	605	Petition to Revive-unintentional	
142	1210	242	605	Utility issue fee (or reissue)	
143	430	243	215	Design issue fee	
144	580	244	290	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Petitions related to provisional applications	
126	240	126	240	Submission of IDS	
581	40	581	40	Recording each patent assignment per property	
146	760	246	380	Filing a submission after final rejection	
149	760	249	380	For each add'l invention to be examined	

Other fee (specify) _____

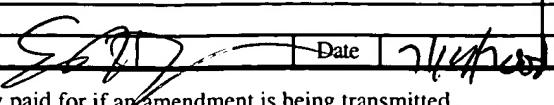
Other fee (specify) _____

*Reduced by Basic Filing Fee Paid

Subtotal (3) (\$).00

SUBMITTED BY

Name ERIC S. HYMAN

Signature 

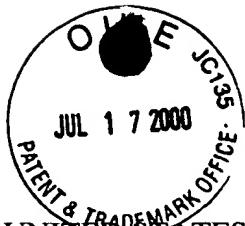
COMPLETE (if applicable)

Reg. Number 30,139

Deposit Acct User ID

*Highest number of claims previously paid for if an amendment is being transmitted.

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Our Ref. No.: 82225.P0189R

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application for:

M. Powell et al.

Serial No.: 08/887,680

Filed: July 3, 1997

For: **METHOD AND APPARATUS FOR
EXTENDING COMPUTER
ARCHITECTURE FROM 32 TO 64 BITS**

Examiner: D. Eng

Art Group: 2783

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RESPONSE TO OFFICE ACTION

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

In an Office Action mailed July 20, 2000, it is noted that the Amendment filed April 10, 2000 proposes amendments to Claim 33 that do not comply with 37 CFR 1.121(b). It is submitted that the following Amendment is responsive to this requirement.

IN THE CLAIMS

1 33. (Amended) A method for context switching a processor that executes procedures
2 having differing word sizes, comprising the steps of:
3 testing a least significant bit of a stack pointer register in the processor that indicates whether a set
4 of data values for a procedure that are stored in a set of registers in the processor, each have a
5 first word size or a second word size, wherein the first word size is less than the second word
6 size;
7 transferring the data values from a least significant portion of each register to a first stack save area
8 in memory if the least significant bit of the stack pointer register indicates the first word size;
9 transferring the data values from the registers to a second stack save area in memory if the least
10 significant bit of the stack pointer register indicates the second word size[;